

Response and Preliminary Amendment  
U.S. Appln. No. 09/685,771

12. (Amended) The substrate as claims in claim 11, further comprising:  
a conductive element which electronically connects said top main surface and said  
bottom main surface.

D 9 13. (Amended) The substrate as claimed in claim 11, further comprising:  
vias which electronically connects said top main surface and said bottom main  
surface.

10 14. (Amended) The substrate as claimed in claim 13, wherein said vias are provided on  
the side portion of said substrate.

11 15. (Amended) The substrate as claimed in claim 11, wherein said surface layers further  
include a side layer which electronically connects said top main surface and said bottom main  
surface.

12 16. (Amended) The substrate as claimed in claim 10, wherein said surface layers include  
six surface layers.

13 17. (Amended) The substrate as claimed in claim 10, further comprising a signal layer  
which is provided between said top main surface and said bottom main surface, and has a pattern  
which is connected to said part of circuit.

14 18. (Amended) The substrate as claimed in claim 10, wherein an interval between  
said part of circuit and said surface layer is defined to prevent said part of circuit from short-  
circuiting.